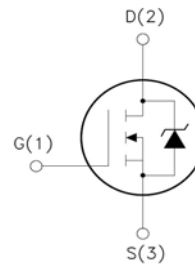
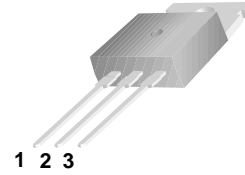



TO-220


- 1. Gate (G)
- 2. Drain (D)
- 3. Source (S)

90N10

Features:

- Low Intrinsic Capacitances.
- Excellent Switching Characteristics.
- Extended Safe Operating Area.
- Unrivalled Gate Charge : $Q_g=60nC$ (Typ.).
- $BVDSS=100V, I_D=90A$
- $R_{DS(on)} : 0.012\Omega$ (Max) @ $V_G=10V$
- 100% Avalanche Tested

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Maximum	Unit
V_{DSS}	Drain-to-Source Voltage	100	V
V_{GSS}	Gate-to-Source Voltage	± 25	V
I_D^3	Continuous Drain Current	$T_C=25^\circ C$	90
		$T_C=100^\circ C$	51
I_{DP}^4	Pulsed Drain Current	$T_C=25^\circ C$	219
I_{AS}^5	Avalanche Current	30	A
EAS^5	Avalanche energy	225	mJ
PD	Maximum Power Dissipation	$T_C=25^\circ C$	166
		$T_C=100^\circ C$	83
T_J, T_{STG}	Junction & Storage Temperature Range	-55~175	$^\circ C$

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta jc}$	Thermal Resistance-Junction to Case	0.9	$^\circ C/W$
$R_{\theta ja}$	Thermal Resistance-Junction to Ambient	62.5	

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	—	—	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=80V, V_{GS}=0V$	—	—	1	uA
		$T_J=125^\circ\text{C}$	—	—	20	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3	4	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$	—	—	± 100	nA
$R_{DS(on)}^1$	Drain-Source On-Resistance	$V_{GS}=10V, I_D=50A$	—	10	12	m Ω
			—	—	—	
Diode Characteristics						
V_{SD}^1	Diode Forward Voltage	$I_{SD}=50A, V_{GS}=0V$	—	—	1.3	V
I_S^3	Diode Continuous Forward Current		—	—	90	A
t_{rr}	Reverse Recovery Time	$I_F=50A,$	—	46	—	nS
Q_{rr}	Reverse Recovery Charge	$di/dt=100A/\mu s$	—	86	—	nC
Dynamic Characteristics²						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V,$ Frequency=1MHz	—	1.2	—	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=25V$ Frequency=1MHz	—	3946	—	pF
C_{oss}	Output Capacitance		—	454	—	
C_{rss}	Reverse Transfer Capacitance		—	295	—	
$t_{d(on)}$	Turn-On Delay Time	$V_{DD}=50V, I_D=30A,$ $V_{GS}=10V, R_G=6.8\Omega$	—	15	—	nS
t_r	Rise Time		—	108	—	
$t_{d(off)}$	Turn-Off Delay Time		—	51	—	
t_f	Fall Time		—	59	—	
Gate Charge Characteristics²						
Q_g	Total Gate Charge	$V_{DS}=50V, V_{GS}=10V$ $I_D=30A$	—	60	—	nC
Q_{gs}	Gate-to-Source Charge		—	13.7	—	
Q_{gd}	Gate-to-Drain Charge		—	22.8	—	

Note: 1: Pulse test; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

2: Guaranteed by design, not subject to production testing.

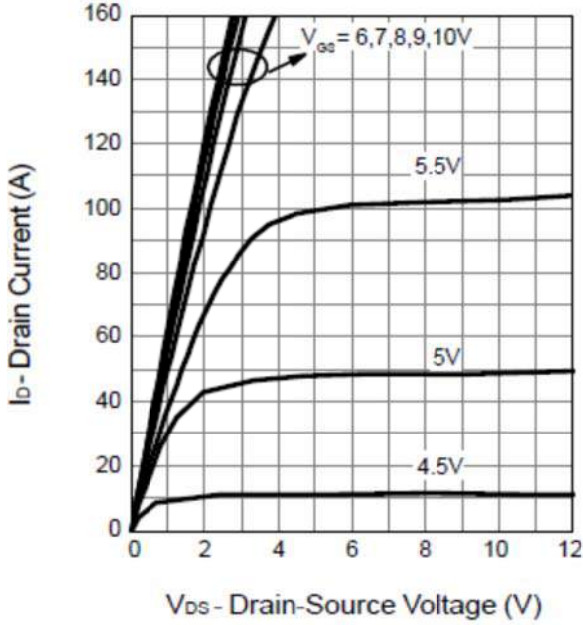
3: Package limitation current is 55A.

4: Repetitive rating, pulse width limited by max junction temperature.

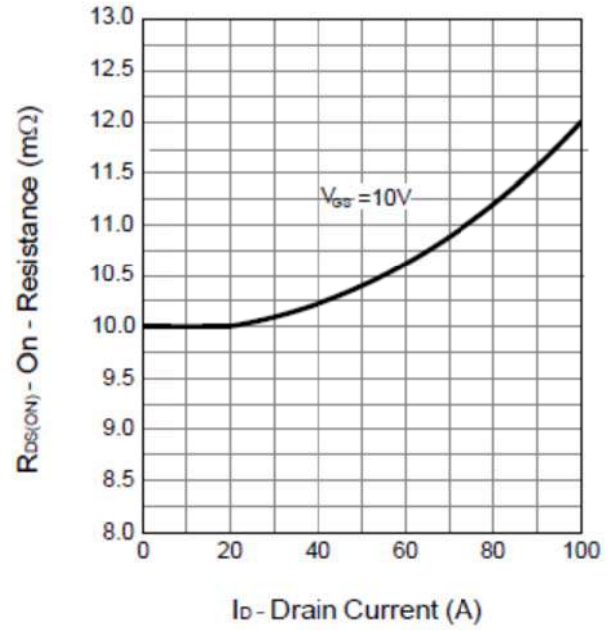
5: Starting $T_J = 25^\circ\text{C}, L = 0.5\text{mH}, I_{AS} = 30A$.

Typical Characteristics

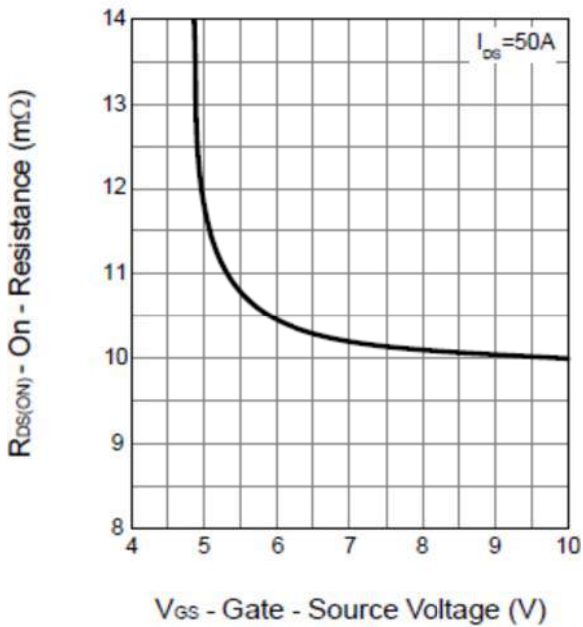
Output Characteristics



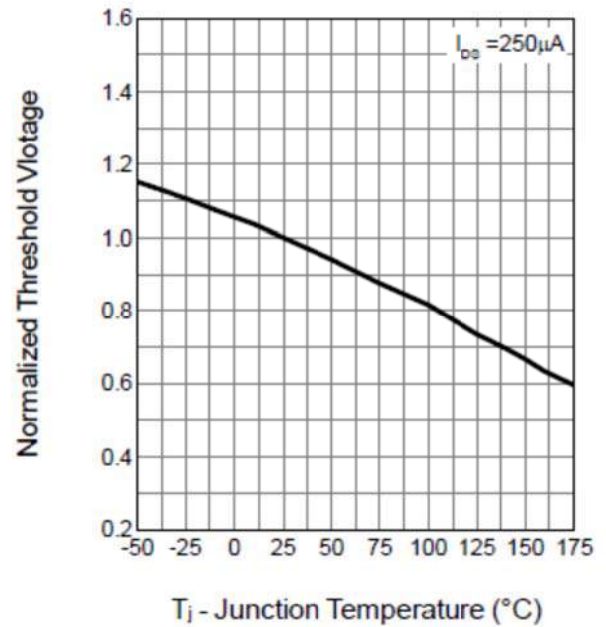
Drain-Source On Resistance



Drain-Source On Resistance

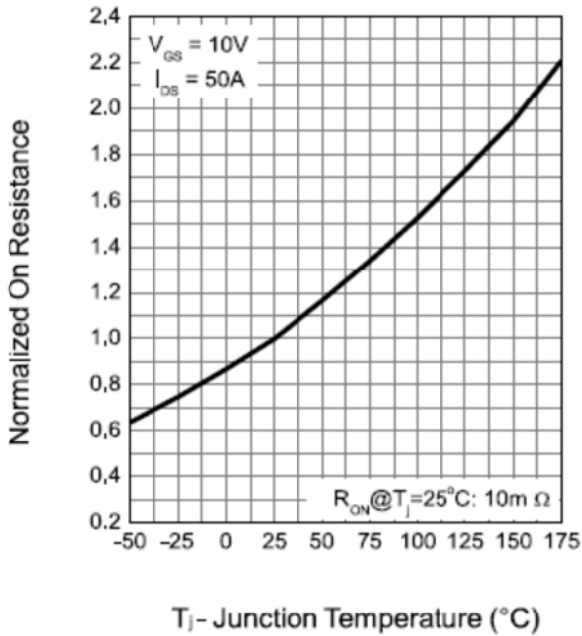


Gate Threshold Voltage

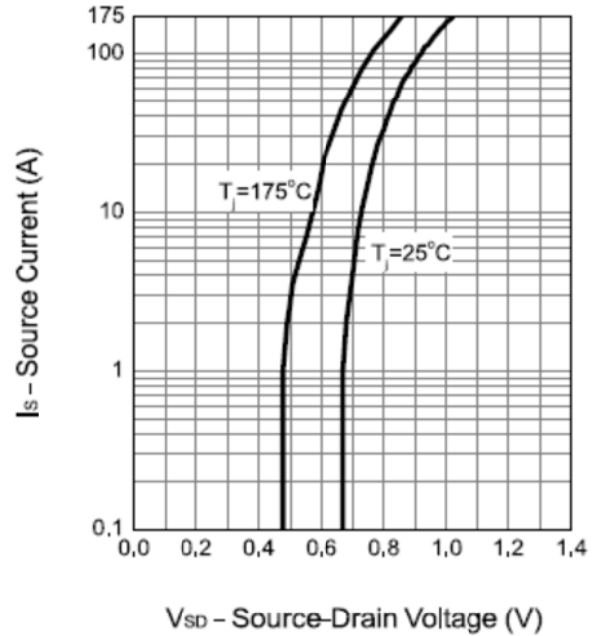


Typical Characteristics (Continued)

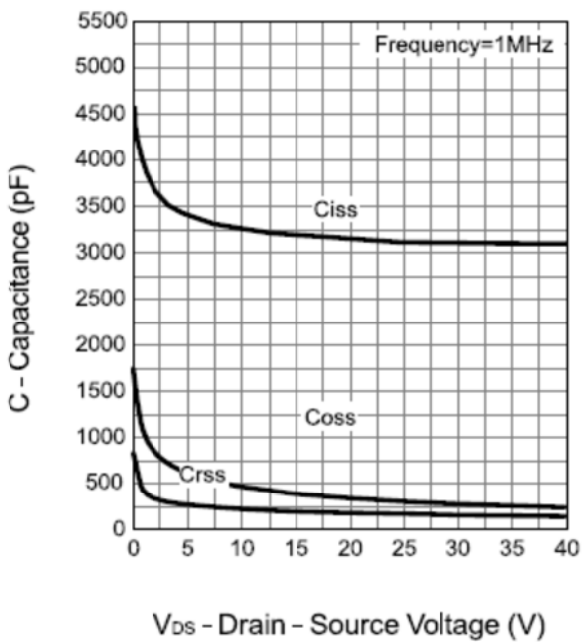
Drain-Source On Resistance



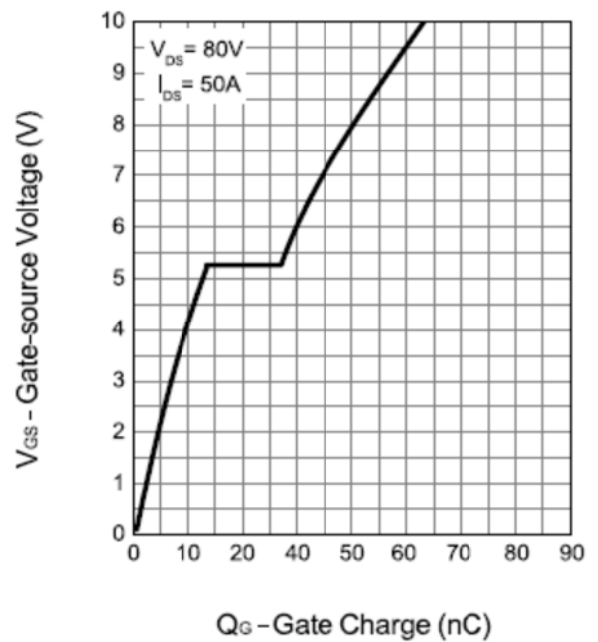
Source-Drain Diode Forward



Capacitance

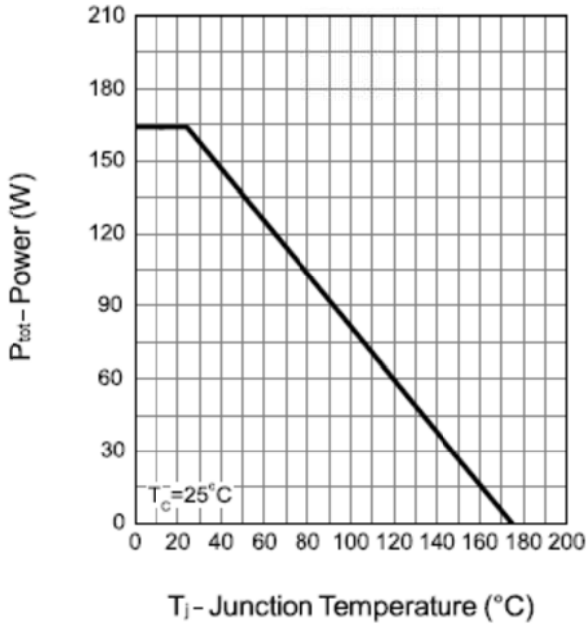


Gate Charge

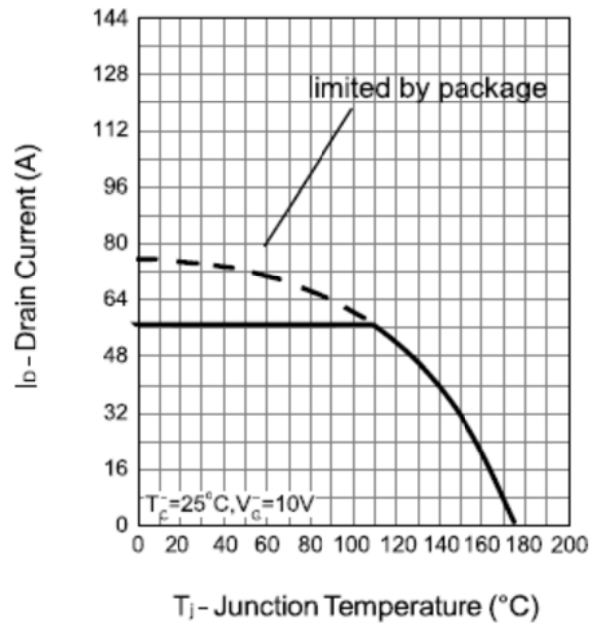


Typical Characteristics (Continued)

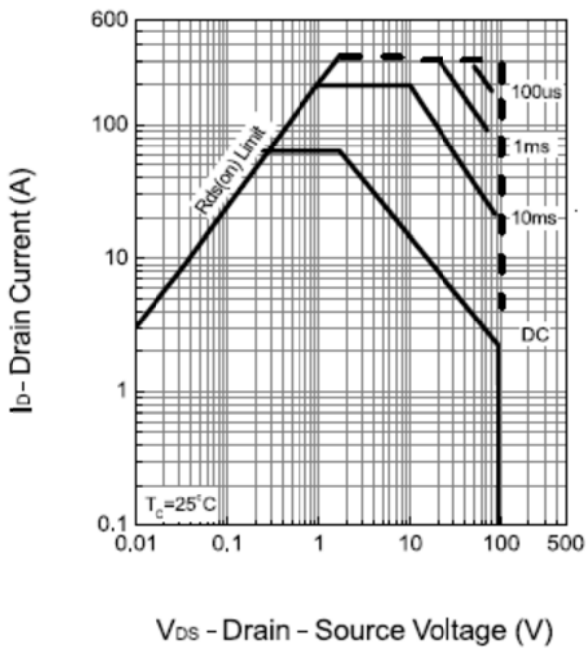
Power Dissipation



Drain Current



Safe Operation Area



Thermal Transient Impedance

